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Optimization and Practice of the Prac[illegible] Digital Electronic Technology

Zongfu Xie, [illegible]njin Liu, Yawei Ji and Bin Yang

University of [illegible], Henan, China

Abstract. In view of the issues in the [illegible]

INTRODUCTION

[illegible]

Corresponding author: Xie Zongfu [illegible]@[illegible].com

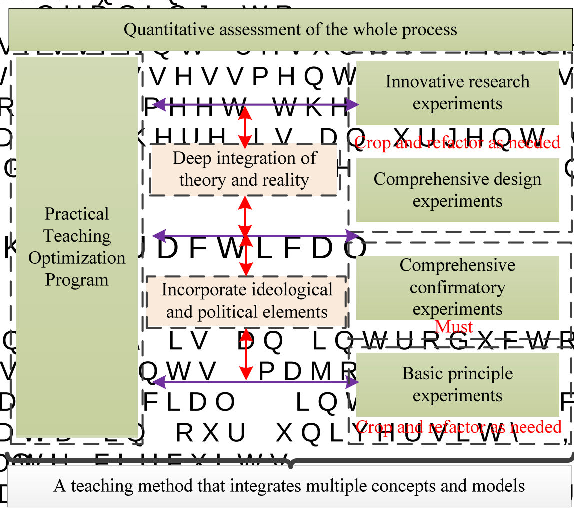
methods which will affect the [illegible] adopt appropriate [illegible] improve the comprehensive quality of in depth study.

The practical teaching [illegible] detailed enough and the whole process cannot be quantitatively assessed. In the past, the practice assessment method focused too much on outcome based assessment and there was little examination of students' ability and quality [ ][ ] in the [illegible] experimentation, the exper[illegible] experimental details are not strict, which leads to [illegible] students' lack of understanding of the experimental principles and processes, and cannot well cultivate students' ability to unite and collaborate and innovate. [illegible] excessive limitations in [illegible] traditional outcome orient[illegible] practical teaching can no [illegible] major talent cultivation [illegible] explore a full process proce[illegible]

[illegible] assessment mechanism of practic[illegible] new ma[illegible]

Optimal design of the practical [illegible] system

To adapt to the practical teaching [illegible] emerging majors, this paper pro[illegible] teaching [illegible] practice [illegible] integrate [illegible] comprehensive assessments [illegible] shown in [illegible]



Optimized Design of the Practical Teaching

The Digital Electronics Tech[illegible] is an introductory and mandatory course for s[illegible] majoring in information engineering, [illegible] unmanned systems and big data [illegible] mainly cover[illegible] combinational and sequenti[illegible] FPGA (Field Programmable Gate Array), [illegible] Complex Programmable Logic Device [illegible] generation and shaping, analog to digital and digital to analog converters, etc. The course has strong theoretical, practical and operational characteristics and plays a crucial role in the overall talent cultivation system [ ].

Table . List of knowledge [illegible] the practical teaching

Category	Practical content	Optimization and Design
The Basic Experiments	Identification [illegible] commonly used [illegible] electronic devices	Tailor and adapt to different majors for different ma[illegible]
	The use of instrumentation	
	The use of EDA tools	
	Design and implementation of typical digital logic circuits	
The Comprehensive Experiments	Experiments [illegible] the course or multiple re[illegible] course	[illegible] integrated [illegible] or desi[illegible]
The Design Experiments	They are both comprehensive [illegible] They mainly focus on the theoretical knowledge	Combined with the [illegible] designs are simple and minimal
The Innovative research experiments	Experiments with the [illegible] exploration are designed [illegible] of different disciplines	[illegible] exploration

In terms of the optimal de[illegible] to improve the innovation [illegible] practical teaching [illegible] nature of practical teaching [illegible] the requirements of different [illegible] and the [illegible]

7DEOH GLYLGHV WKH ZKROH SUDFWLFDO
IRXU OHYHOV WKH EDVLF H[SHU
H[SHULPHQWV WKH GHVLJQ H[SH
UHVHDUFK H[SHULPHQWV 7KH G
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,QWHJUDWLRQ RI PXOWLSOH WHDFKLQJ PRGHV

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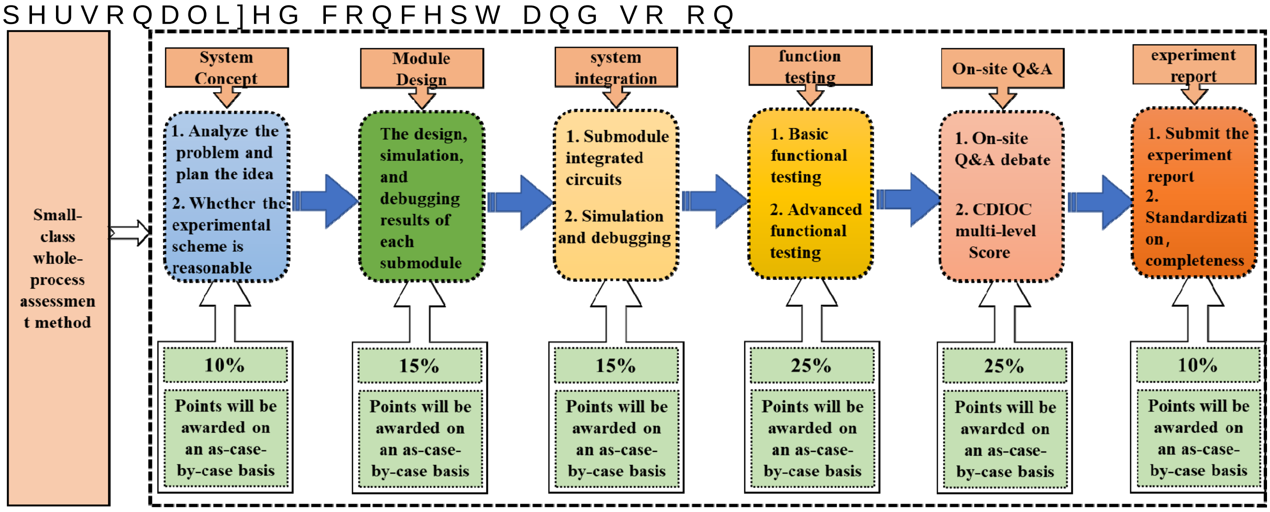

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DQG GHVLJQHG D VHW RI
DVVHVVPHQW PHFKDQLVP DV VKRZQ
7KH DVVHVVPHQW PHFKDQLVP
TXDQWLWDWLYHO\ HYDOXDWLQJ
RI D PRGHUQ GLJLWDO V\VWHP
PRGXOH GHVLJQ V\VWHP LQWHJUDWLRQ
VLWH 4 $ DQG H[SHULPHQW
SHUVRQDOL]HG LQVSHFWLRQ
EDVHG RQ GLIIHUHQW SUDFWLFDO
HYDOXDWH VWXGHQWV DELOLWLHV
RI DELOLW\ PDLQO\ IRFXVHV RQ
H[SHULPHQW FRPSOHWLRQ DQG
VWXGHQWV LQ WKH DERYH VL[
DVVHVVPHQW RI TXDOLW\ PDLQO\
RI LQQRYDWLYH DQG EUHDNWKURXJK
WKH VHQVH RI UHVSRQVLELOLW\

7HDFKLQJ HYDOXDWLRQ PRGHO

7KH FRPSUHKHQVLYH H[SHULPHQW
FRPELQDWLRQDO ORJLF FLUFXLWV

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